

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	12	("5945836") or ("6005405") or ("6292003") or ("3225817") or ("5945838").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/22 07:20
L3	583	(pcb or (printed adj circuit adj board) and (decoupling adj capacitors)) and 324/754-755,761-762.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:20
L5	108	L3 and pin and bga	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:20
L7	21	439/66-72.ccls. and multilayer and bga and pin	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:20
L9	313	multilayer and (pcb or printed adj circuit adj board) and capacitor and bga	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:20
L11	11	L9 and 324/754,755,761,762.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:20
L13	14662	multilayer and (pcb or printed adj circuit adj board)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:20
L15	155	L13 and 324/754,755,761,762.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:20
L17	163	L13 and 439/66-72.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:20
L19	1700	multilayer and (pcb or printed adj circuit adj board) and (pins or probes) and (holes or vias) and power and ground	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:20

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L21	299	L19 and bga	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:21
L23	0	Isi near4 socket near4 test with (pin or probe) and pcb	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:21
L25	129	Isi with (pin or probe) and pcb	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:21
L27	25	L25 and "324"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:21
L29	2	printed adj board and capacitors and power and pogo adj pin and holes and bga and Isi	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:21
L31	7	form adj factor.as. and capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:21
L33	2644	pcb with capacitors	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:21
L35	906	L33 and (pins or probes)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:21
L37	260	pcb with (decoupling adj capacitors)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:21
L39	165	L37 and (pins or probes)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:21

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L41	2373	intel.as. and capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:21
L43	302	intel.as. and decoupl\$3 adj capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:21
L45	10	intel.as. and (decoupl\$3 adj capacitor) near4 board	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:21
L47	2	intel.as. and (decoupl\$3 adj capacitor) near4 board and probes	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:21

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L6	3	board with decoupling adj capacitors and pogo adj pin and through adj hole and power.clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/22 07:28